

Dante Chipsets

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Dante Interface	Maximum Channels	Dante Flows	Redundant Interface	Can Serve as Clock Leader
Ultimo	0x4 2x2 4x0	2		✓ ≤20
Broadway	16x16	16	✓	✓
Brooklyn II	64x64	32	✓	✓ ≤250
PCIe Card	128x128	32	✓	✓
HC	512x512	128	✓	✓
Dante AV	V: 1x0 or 0x1 A: 8x8	V: 1 A: 4		✓
DVS	64x64	16		
Dante Via	16x16/program 32x32 total	8		✓

Dante Domain Manager can arrange clocking trees to support over a thousand Dante devices, so one device does not have to synchronize all others directly.